Advanced Fault-Tolerant Control for Switched Reluctance Motor Drives McMaster Institute for Automotive Research and Technology (MacAUTO)

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OPERATING PRINCIPAL	
The proposed method has an additional two controlled switches and six thyristors are used.	 Cor ope
The upper switch SR+ and the three thyristors T_{A+} , T_{B+} and T_{C+} tolerate the upper switch faults.	 Rec con
The lower switch $S_{R_{-}}$ and the three thyristors $T_{A_{-}}$, $T_{B_{-}}$ and $T_{C_{-}}$ tolerate the lower switch faults.	 Car fau

SEXTUPLE SWITCH FAULTS

The three phases suffer from positive voltage instead of negative during chopping at low speeds and during commutation at high speeds which results in high currents.

The conduction periods of the three phases are reduced to avoid the current increase.





t(s)

ltem	High Speed	Low Speed
$\boldsymbol{\theta}_{on_abc}$	27	24
$\boldsymbol{\theta}_{off_a,b}$	37	44
$\boldsymbol{\theta}_{off_c}$	42	44
T_{avg} (N.m)	5	5
∆ <i>T_{ripple} (%)</i>	138	40
N _{rpm_ref}	4100	600
N _{rpm_real}	3754	597.5



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ADVANTAGES

mplete fault-tolerance at low speeds up to four en-switch faults.

of additional switches duced number mpared to other methods.

n work if all switches suffer from open-switch lt.

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